



1281.63407

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Munir H. Nayfeh
Serial No.: 09/496,506
Conf. No.: 6344
Filed: February 2, 2000
For: SILICON NANOPARTICLE FIELD
EFFECT TRANSISTOR AND
TRANSISTOR MEMORY DEVICE
Art Unit: 2811
Examiner: Sara W. Crane

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Mail Stop AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

May 16, 2005

Date
F-CLASS.WCM

Registration No. 43,874

Appr. February 20, 1998

Attorney for Applicant

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL

Dear Sir:

Enclosed please find the following:

- (X) Response to Notice of Incomplete Reply.
- (X) Brus Reference, pages 5-7.
- (X) Copy of Notice of Incomplete Reply.
- (X) If a Petition under 37 C.F.R. §1.136(a) for an extension of time for response is required to make the attached response timely and does not separately accompany this transmittal, Applicant(s) hereby petition(s) under 37 C.F.R. §1.136(a) for an extension of time for response in the above-identified application for the period required to make the attached response timely.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

Customer No. 24978

May 16, 2005

300 South Wacker Drive
Suite 2500
Chicago, Illinois 60606
Tel: (312) 360-0080
Fax: (312) 360-9315
P:\DOCS\1201\63407\941875.DOC

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By:

Arik B. Ranson
Registration No. 43,874